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PART 2

Review

- **Levels of abstraction**
- **Entity and Architecture**
- **Signal assignments**
- **Guarded signal assignments**
- **Three state bussing**
- **Process statements**
- **Combinational processes**
- **Sequential processes**
- **Multiplexing**
- **Package**

In General:

We will develop parts
For the MSI based
design of Part 3

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Review.VHDL

VHDL

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ERROR: syntaxerror
OFFENDING COMMAND: ----nostringval----

STACK:

0
-0.213