

Bilinear Filtering – Part #2

- Add more datapath resources to the starting datapath for Part #1 to reduce the number of clocks
 - Single SRAM is still a constraint
 - Interface does not change
 - Can add more multipliers/satadd – you decide how many and where
 - Cannot use more than 16 states in FSM
 - Total computation for 8 Tnew values must be less than 50 clocks
 - You will have to change the datapath and your FSM
 - 2nd part is worth 200 pts

Testing

- Can use the same testbench from Part #1 to test your design.
 - Zip archive contains a single file called *tb_bifiltp2_gold.scf* which is my solution.
 - If you equal or beat this in terms of # of clocks, get 10 points added to any test grade.
- See first part for due dates on Lab